## ANALOG COMPUTATION CIRCUITS

MODEL NUMBER		SMALL SIGNAL BW MHZ	FULL POWER BW	SLEW RATE V/usec	TIME typ usec			NONLINEARITY				CMRR	WIDEBAND NOISE		Ι%	POWER	Model Designator			•	
						ERROR			Y Input	Y=20 X=0	X=20 Y=0			RMS typ I0 HZ	AMPLITUDE ERROR	SUPPLIES	Т	empe	rature		
						@25C @Tma		X Input					I0 HZ				Range			PRICE	
						% FS	% FS	% FS	% FS	mV	mV	dB	to 10 KHZ	to 5 MHZ	KHZ		0		-40		
																	70	85	85	125	
ANALO	G MULTIPLIERS																				
AD532	Vout=(X1-X2)*(Y1-Y2)/10	1	75KHz	45 typ	1 typ	2	NS	0.8	0.3	200	150	40	0.6	3	75 typ	± 15V@ 6 mA	J				\$15.80
AD532	()				- 71	1		0.5	0.2	100	80	50			71		K			S	\$33.10
AD632	Vout=(X1-X2)*(Y1-Y2)/10+Z2	1	75KHz	20 typ	2 typ	1	1.5	.4 typ	0.2	ns	ns	60	0.09	1	50 typ	± 15V@ 6 mA		Α		S	\$17.50
AD632					71	0.5	1	.3 max	.1 max	60	20	70			31			В		T	\$29.95
AD534	Vout=(X1-X2)*(Y1-Y2)/10-(Z1+Z2)	1	NS	20	2	±1	NS	NS	NS	NS	NS	60	0.09	1	50	± 15V@ 6 mA	J				\$16.95
AD534						±.5		0.3	0.1	20	60	70					K				\$32.00
AD534						±25		0.12	0.1	20	24						L				\$54.40
AD534						±1		NS	NS	NS	NS	60								S	\$69.70
AD534						±.5		0.3	0.1	20	60	70								T	\$93.30
AD633	Vout=(X1-X2)*(Y1-Y2)/10+Z	1	NS	20	2	±2	NS	1	0.4	200	80	60	0.09	1	50	± 15V@ 6 mA	J				\$3.75
		-3dB								Y=10	X=10										
AD734	Vout=(X1-X2)*(Y1-Y2)/(U1-U2)-(Z1-Z				100 to 1%	±1	1	NS	NS	5	10	70	-88dBC			± 15V@ 12 mA			A		\$12.66
AD734		<del>-</del>			200 to .1%		0.6			1.5	3	, ,							В		\$21.43
AD734						±4	1.25			5	10									S	*
AD538	Vout=Vy*(Vz/Vz)^m	0.4		1.4		±1	2									± 15V@ 7 mA		Α			\$27.86
AD538						±.5	1											В			\$35.38
AD538						±1	2.5													S	\$59.56
	gle or Differential, Current Output	-3dB								f <i mhz<="" td=""><td>Z, Vy=I.5V</td><td>rms</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>-</td><td>******</td></i>	Z, Vy=I.5V	rms								-	******
AD539	V01=-Vx*Vy1	50				±2.5				-75 dB						+15 @ 10.2 mA	J				\$17.15
AD539	V02=-Vx*Vy2					±1.5				75 00						-15 @ 22.2 mA	K				\$25.14
AD539	1 0 1 1 1 1 1					±2.5														S	\$83.00
	fferential Current Output									Y=+ 1V	X=± 1V										400.00
AD834	Vout= $Vx*Vy/1V^2 \pm 4mA$	500				±2				6	4					±5V @ 14/35 mA	J		A	S	\$16.00
	ngle Ended Voltage Output	300				12				- 0	7					25 T G 1-455 Hz 1	,		21		410.00
AD835	Vout=Vx*Vy+Vz	200	55		I7nSEC	±5	±3	0.7	0.5	1	1	80	45 nV/HZ	7					A		\$8.95
ADOSS	vout-vx·vy+vz	200	33		T/HSEC	Ξ.)	±3	0.7	0.5	1	1	80	4.5 H V/112						A		\$0.23
MLT04. C	mad									V-+ 2.5V	X=± 2.5V										
MLT04	Vout=Vx*Vy/2.5V	8	2	30	1	±5	0.25	1	1	1 -1 2.3 v	NS NS	65	78	380		±5V @ ±20mA			G		\$11.95
	177 17725			50	1		0120	-	-	1	1.0		,,,	500							41100
Balance	d Modulator/Demodualtor															POWER	Mod	el Des	ignato		
	TRANSFER	Open			Comp	Comp	Unity	Slew	Settling	CMRR	PSRR					SUPPLIES	Т	Temperature			
	FUNCTION	Loop			Response	Switch	Gain	Rate	Time									Rang	ge		PRICE
		Gain	Eos	Ib	Time	Window											0	-25	-40	-55	
		dB	mV	nA	nsec	mV	MHz	V/usec	usec	dB	dB						70	85		125	I00's
AD630	Vout=G*Vin*±1	90	0.5	300	200	±2	2	45	3	85	90					±5>16.5V, ±5mA	J	A		S	
AD630		100	0.1							90						,	K	В			
Analog '	Trig Function Generator																				
AD639	See Data Sheet for Spec's																	A		S	\$29.29
AD639	See Data Sheet for Spec's																	В			\$44.08
	•																				